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APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*  
 This application is a DIV of 09/705,897 11/06/2000 PAT 6,717,262 *dz*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*  
 JAPAN 2000-029927 (PAT. 02/08/2000) *dz*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
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Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>dz</i> Examiner's Signature Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 9	TOTAL CLAIMS 10	INDEPENDENT CLAIMS 1
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TITLE  
 Mounted circuit substrate and method for fabricating the same for surface layer pads that can withstand pad erosion by molten solder applied over a plurality of times

FILING FEE	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of
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